



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al.                      Art Unit : 2815  
Serial No. : 09/736,724                      Examiner : Paul E. Brock  
Filed : December 13, 2000  
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TWO-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated March 31, 2004, be extended for two months to and including August 30, 2004.

Enclosed is a check for \$420 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

*August 30, 2004*

*William G. Hughes, Jr.*

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